

Group Art Unit: 2833  
Examiner: Luebke, R.

Atty. Ref.: FP03-013US

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant : Kazunori Yamashita  
Appl. No. : 10/611,762  
Filed : July 1, 2003  
For : CIRCUIT BOARD CONNECTOR AND METHOD OF ASSEMBLING IT

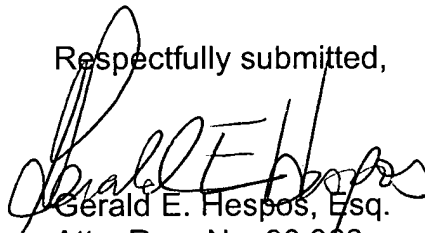
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA, 22313-1450

**ELECTION**

Sir:

This Election is submitted in response to the Restriction Requirement of January 2, 2004. The applicant hereby elects claims 1-9 which are drawn to an electrical connector. This election is made without traverse.

Respectfully submitted,



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Date: January 15, 2004

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:

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